

Figure 1. System Block Diagram

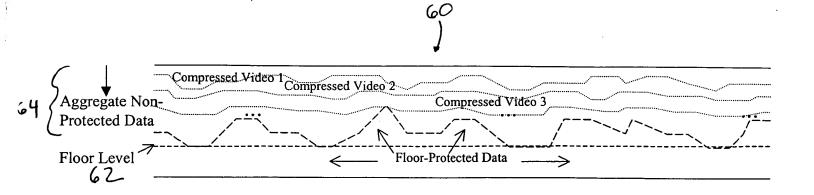


Figure 2. Carrier Channel Using One Floor Level

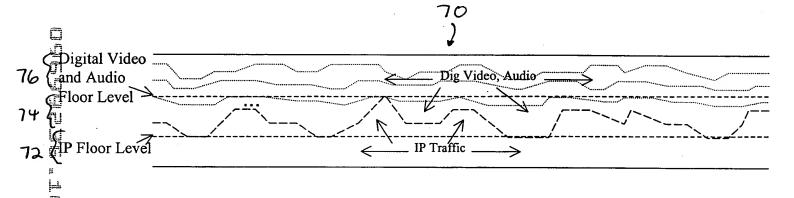


Figure 3. Carrier Channel Using Two Floor Levels